

Integrating Error Detection into Arithmetic Coding

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October 27, 1995

Abstract

A technique to implement error detection as part of the arithmetic coding process is described. Heuristic arguments are given to show that a small amount of extra redundancy can be very effective in detecting errors very quickly, and practical tests confirm this prediction.

*On leave from the School of Engineering, University of Manchester, UK. Funded by UK's Engineering and Physical Sciences Research Council (grant GR/J98790).

Error Coding For Arithmetic Processors

Daniel Siewiorek, Robert Swarz



Error Coding For Arithmetic Processors:

Error Coding for Arithmetic Processors T.R.N. Rao,1974-01-01 *Error Coding for Arithmetic Processors* provides an understanding of arithmetically invariant codes as a primary technique of fault tolerant computing by discussing the progress in arithmetic coding theory The book provides an introduction to arithmetic error code single error detection and long distance codes It also discusses algebraic structures linear congruences and residues Organized into eight chapters this volume begins with an overview of the mathematical background in number theory algebra and error control techniques It then explains the basic mathematical models on a register and its number representation system The reader is also introduced to arithmetic processors as well as to error control techniques The text also explores the functional units of a digital computer including control unit arithmetic processor memory unit program unit and input output unit Students in advanced undergraduate or graduate level courses researchers and readers who are interested in applicable knowledge on arithmetic codes will find this book extremely useful

ERROR CODING FOR ARITHMETIC PROCESSORS

Thammavarapu R. N. Rao,1974 *Concurrent Error-detecting Codes for Arithmetic Processors* Raymond S. Lim,1979

Concurrent Error Detecting Codes for Arithmetic Processors National Aeronautics and Space Administration (NASA),2018-07-25 A method of concurrent error detection for arithmetic processors is described Low cost residue codes with check length l and checkbase $m = 2^l - 1$ are described for checking arithmetic operations of addition subtraction multiplication division complement shift and rotate Of the three number representations the signed magnitude representation is preferred for residue checking Two methods of residue generation are described the standard method of using modulo m adders and the method of using a self testing residue tree A simple single bit parity check code is described for checking the logical operations of XOR OR and AND and also the arithmetic operations of complement shift and rotate For checking complement shift and rotate the single bit parity check code is simpler to implement than the residue codes Lim

R S Ames Research Center NASA TP 1528 A 7810 RTOP 366 18 50 00 00 **Error Detecting and Correcting Codes for Arithmetic Processors** Joseph Charles Circello,1977 **Application Specific Processors** Earl E. Swartzlander

Jr.,2012-12-06 *Application Specific Processors* is written for use by engineers who are developing specialized systems application specific systems Traditionally most high performance signal processors have been realized with application specific processors The explanation is that application specific processors can be tailored to exactly match the usually very demanding application requirements The result is that no processing power is wasted for unnecessary capabilities and maximum performance is achieved A disadvantage is that such processors have been expensive to design since each is a unique design that is customized to the specific application In the last decade computer aided design systems have been developed to facilitate the development of application specific integrated circuits The success of such ASIC CAD systems suggests that it should be possible to streamline the process of application specific processor design Application Specific

Processors consists of eight chapters which provide a mixture of techniques and examples that relate to application specific processing The inclusion of techniques is expected to suggest additional research and to assist those who are faced with the requirement to implement efficient application specific processors The examples illustrate the application of the concepts and demonstrate the efficiency that can be achieved via application specific processors The chapters were written by members and former members of the application specific processing group at the University of Texas at Austin The first five chapters relate to specific arithmetic which often is the key to achieving high performance in application specific processors The next two chapters focus on signal processing systems and the final chapter examines the interconnection of possibly disparate elements to create systems

1971 International Symposium on Fault-Tolerant Computing ,1971

Reliable Computer Systems Daniel Siewiorek,Robert Swarz,2014-06-28 Enhance your hardware software reliability Enhancement of system reliability has been a major concern of computer users and designers and this major revision of the 1982 classic meets users continuing need for practical information on this pressing topic Included are case studies of reliable systems from manufacturers such as Tandem Stratus IBM and Digital as well as coverage of special systems such as the Galileo Orbiter fault protection system and AT T telephone switching processors

Application Specific Processors Earl E. Swartzlander,1997

Application Specific Processors consists of eight chapters which provide a mixture of techniques and examples that relate to application specific processing The inclusion of techniques is expected to suggest additional research and to assist those who are faced with the requirement to implement efficient application specific processors The examples illustrate the application of the concepts and demonstrate the efficiency that can be achieved via application specific processors The chapters were written by members and former members of the application specific processing group at the University of Texas at Austin

Error-correcting-codes in Computer Arithmetic Chao-Kai Liu,1972

NBS Special Publication ,1968

1972 International Symposium on Fault-Tolerant Computing ,1972

Robotic Systems and

AMT Gideon Halevi,Roland Weill,I. Yudilevich,1990 A team of international researchers and top level professionals contribute to this book which analyzes the complexities of computer integrated manufacturing CIM and Factory of the Future technologies not only for big industrial concerns but also for mid size and small industrial systems Among the topics discussed are managerial strategies integrative tools and various aspects of the implementation of information technology in production operations The largest of the six sections of the book is concerned with the current state of research in robotics

Proceedings of the International Conference on Application Specific Array Processors Sun Yuan Kung,1990

The State of the Art: from Device Testing to Reconfigurable Systems ,1973

Digest of Papers ,1975

International Aerospace Abstracts ,1980

Who's who in Technology Today ,1980

NASA Tech Brief United

States. National Aeronautics and Space Administration. Technology Utilization Division,1973

Proceedings ,1971

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