COMPUTER-AIDED DESIGN AND DIAGNOSIS METHODS FOR BIOMEDICAL APPLICATIONS

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Formal Methods In Computeraided Design

Mandayam Srivas,Albert Camilleri

Formal Methods In Computeraided Design:

    Formal Methods in Computer-Aided Design Ganesh Gopalakrishnan,Phillip Windley,2003-07-31 This book constitutes the refereed proceedings of the Second International Conference on Formal Methods in Computer Aided Design FMCAD 98 held in Palo Alto California USA in November 1998 The 27 revised full papers presented were carefully reviewed and selected from a total of 55 submissions Also included are four tools papers and four invited contributions The papers present the state of the art in formal verification methods for digital circuits and systems including processors custom VLSI circuits microcode and reactive software From the methodological point of view binary decision diagrams model checking symbolic reasoning symbolic simulation and abstraction methods are covered **Formal Methods in Computer-Aided Design** Warren A. Jr. Hunt,Steven D. Johnson,2007-11-29 The biannual Formal Methods in Computer Aided Design conference FMCAD 2000 is the third in a series of conferences under that title devoted to the use of discrete mathematical methods for the analysis of computer hardware and so ware The work reported in this book describes the use of modeling languages and their associated automated analysis tools to specify and verify computing systems Functional veric ation has become one of the principal costs in a modern computer design e ort In addition verica tion of circuit models timing power etc requires even more eo rt FMCAD provides a venue for academic and dustrial researchers and practitioners to share their ideas and experiences of using discrete mathematical modeling and veric ation It is noted with interest by the conference chairmen how this area has grown from just a few people 15 years ago to a vibrant area of research development and deployment It is clear that these methods are helping reduce the cost of designing computing systems As an example of this potential cost reduction we have invited David Russino of Advanced Micro Devices Inc to describe his veric ation of oating point gorithms being used in AMD microprocessors The program includes 30 regular presentations selected from 63 submitted papers **Formal Methods in Computer-Aided Design** Mark D. Aagaard,John W. O'Leary,2003-06-30 This volume contains the proceedings of the Fourth Biennial Conference on F mal Methods in Computer Aided Design FMCAD The conference is devoted to the use of mathematical methods for the analysis of digital hardware c cuits and systems The workreported in this bookdescribes the use of formal mathematics and associated tools to design and verify digital hardware systems Functional veri cation has become one of the principal costs in a modern computer design e ort FMCAD provides a venue for academic and industrial researchers and practitioners to share their ideas and experiences of using screte mathematical modeling and veri cation Over the past 20 years this area has grown from just a few academic researchers to a vibrant worldwide com nity of people from both academia and industry This volume includes 23 papers selected from the 47 submitted papers each of which was reviewed by at least three program committee members The history of FMCAD dates backto 1984 when the earliest meetings on this topic occurred as part of IFIP WG10 2 Formal Methods in Computer-Aided Design Alan J. Hu,Andrew K. Martin,2005-01-18 These are the proceedings of the fifth international conference Formal Methods in

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